

Address Generation	Read Instruction	Pre- Decode	Instruction Decode	Decode Dispatch	Operand Address Generation	Fetch Operation	Execute Instructions	Store Results
OY	1Y	2Y	3Y	1X	2X	3X	4X	5X
								6X

*FIG. 1*

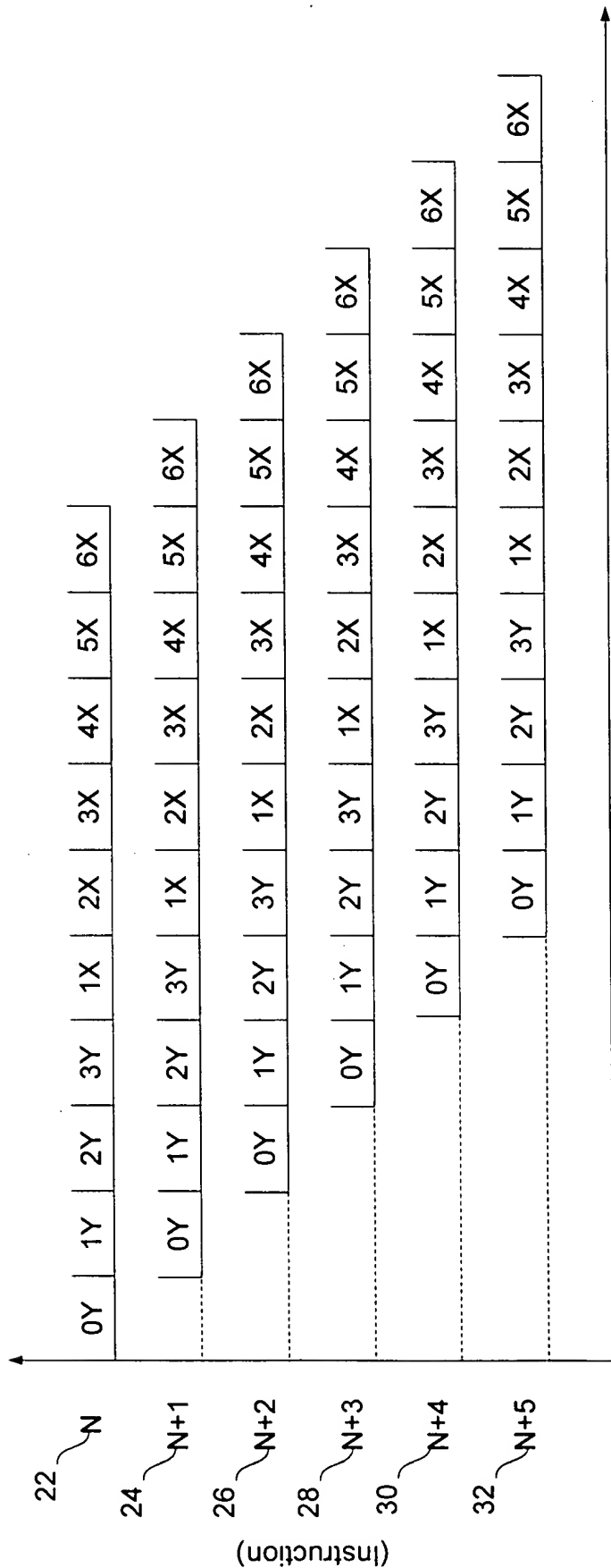
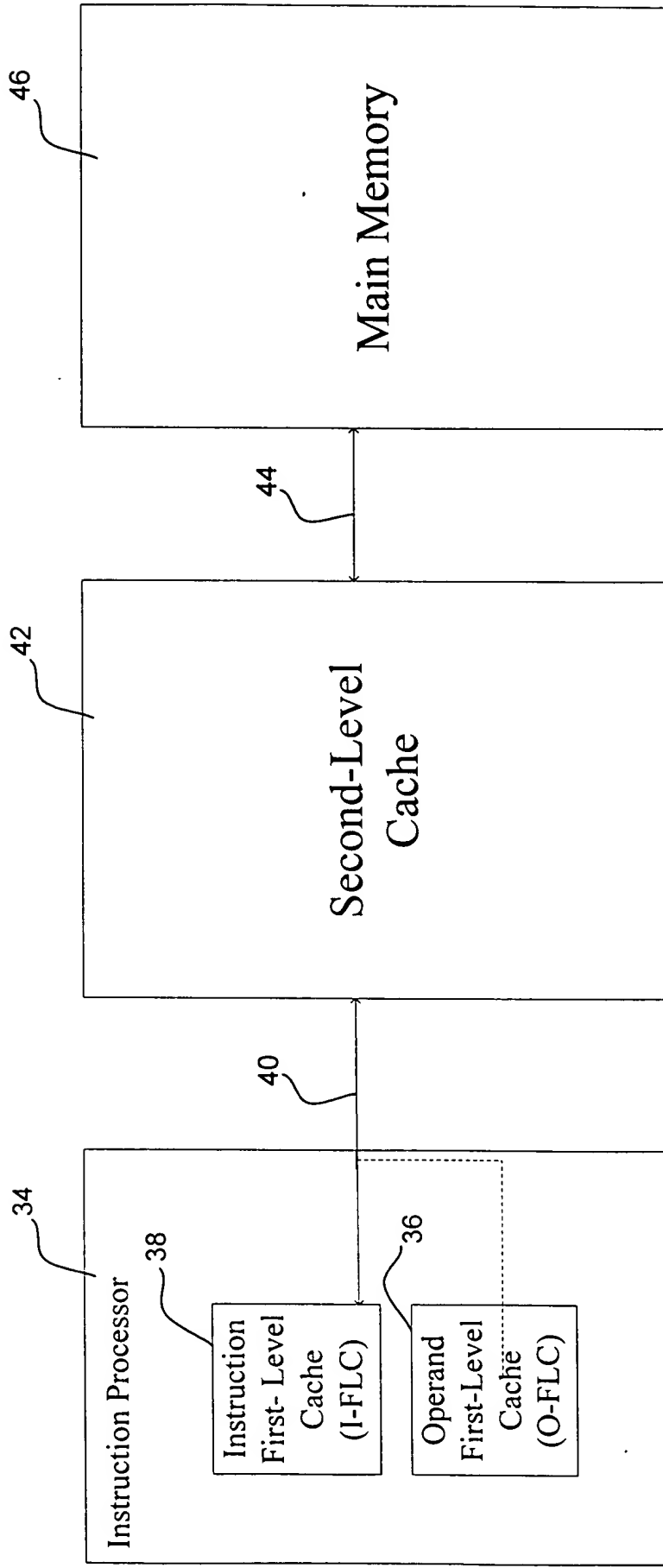
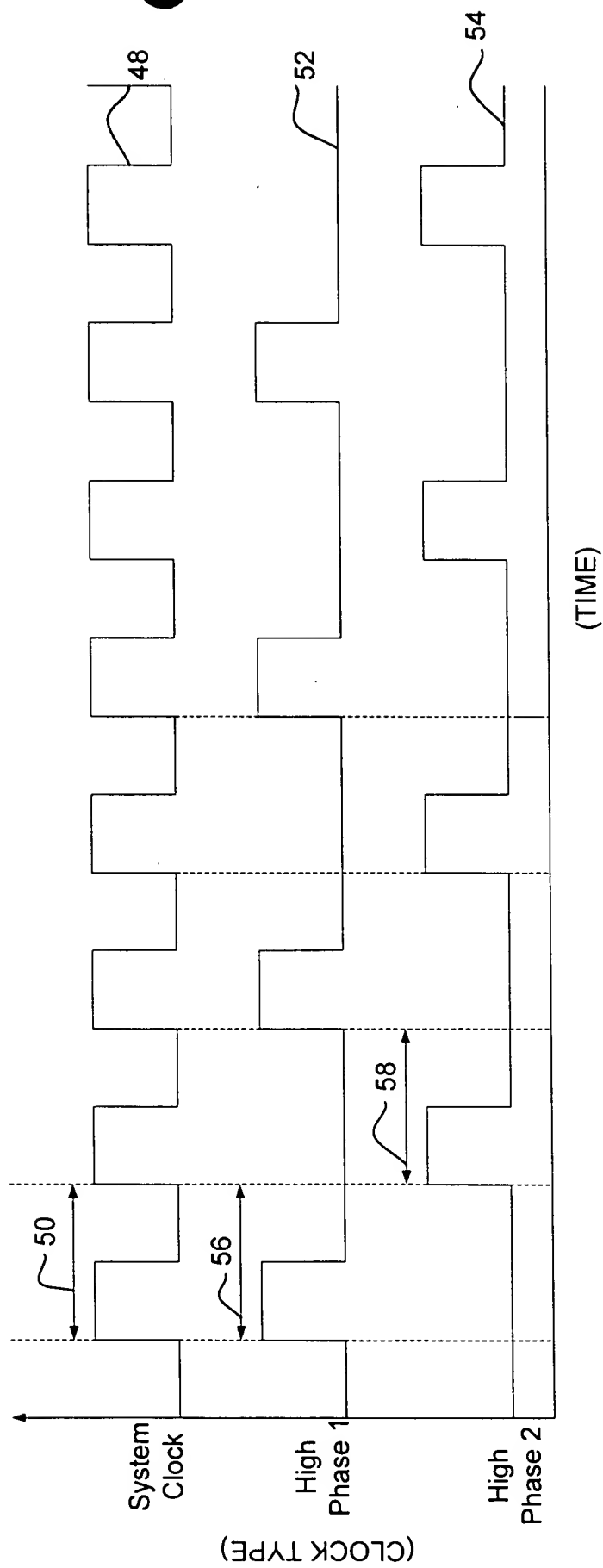


FIG. 2



*FIG. 3*



**FIG. 4**

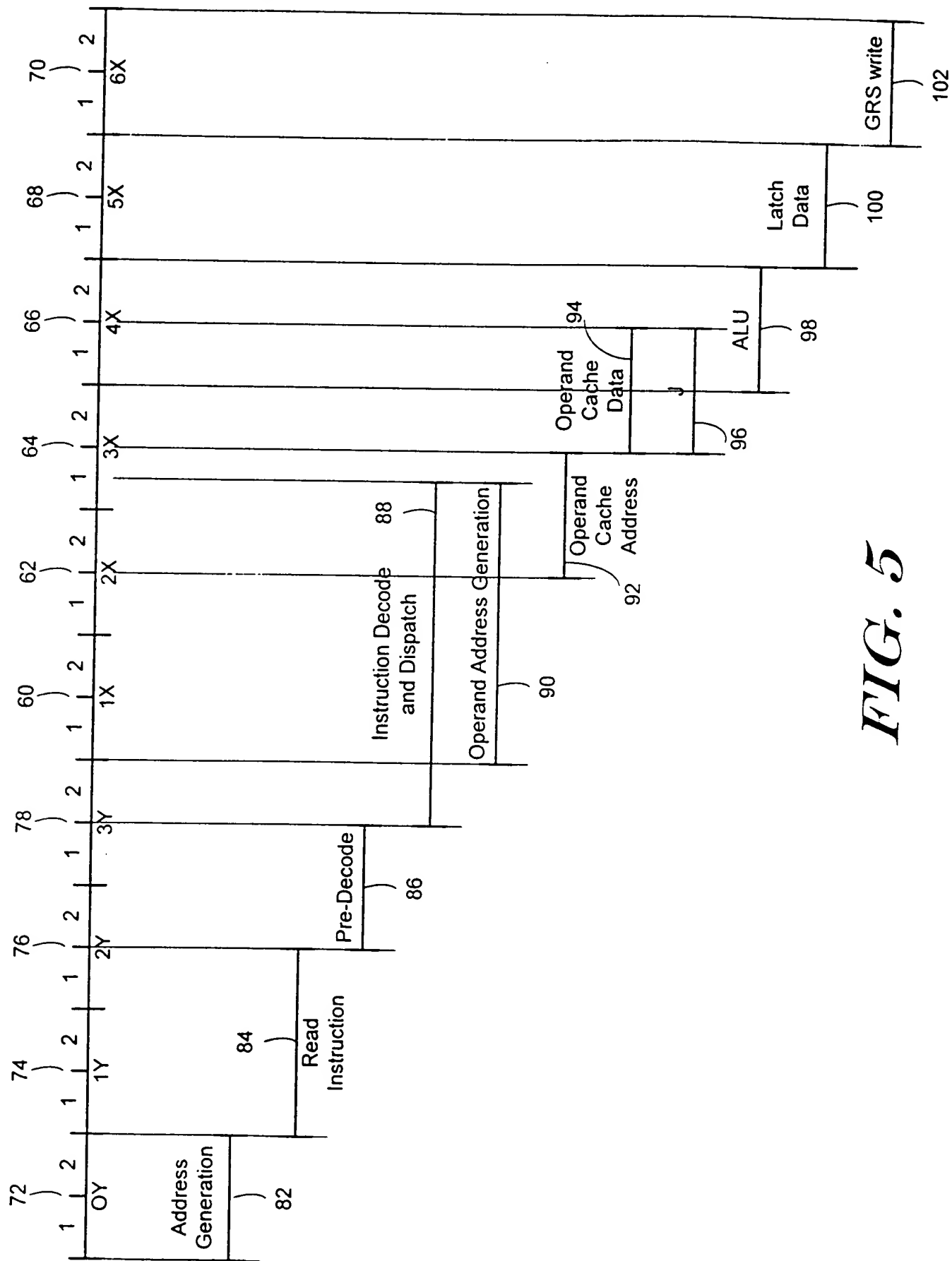


FIG. 5

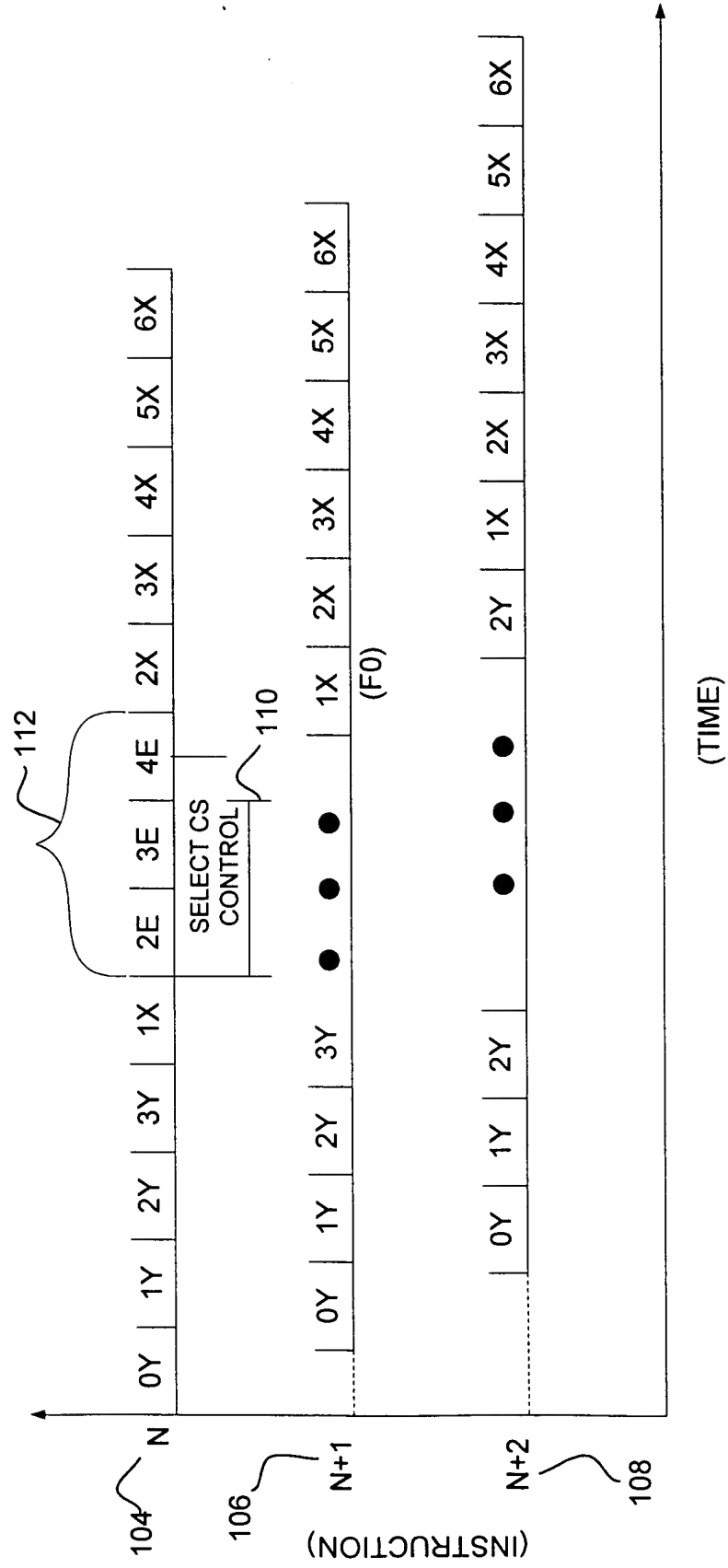
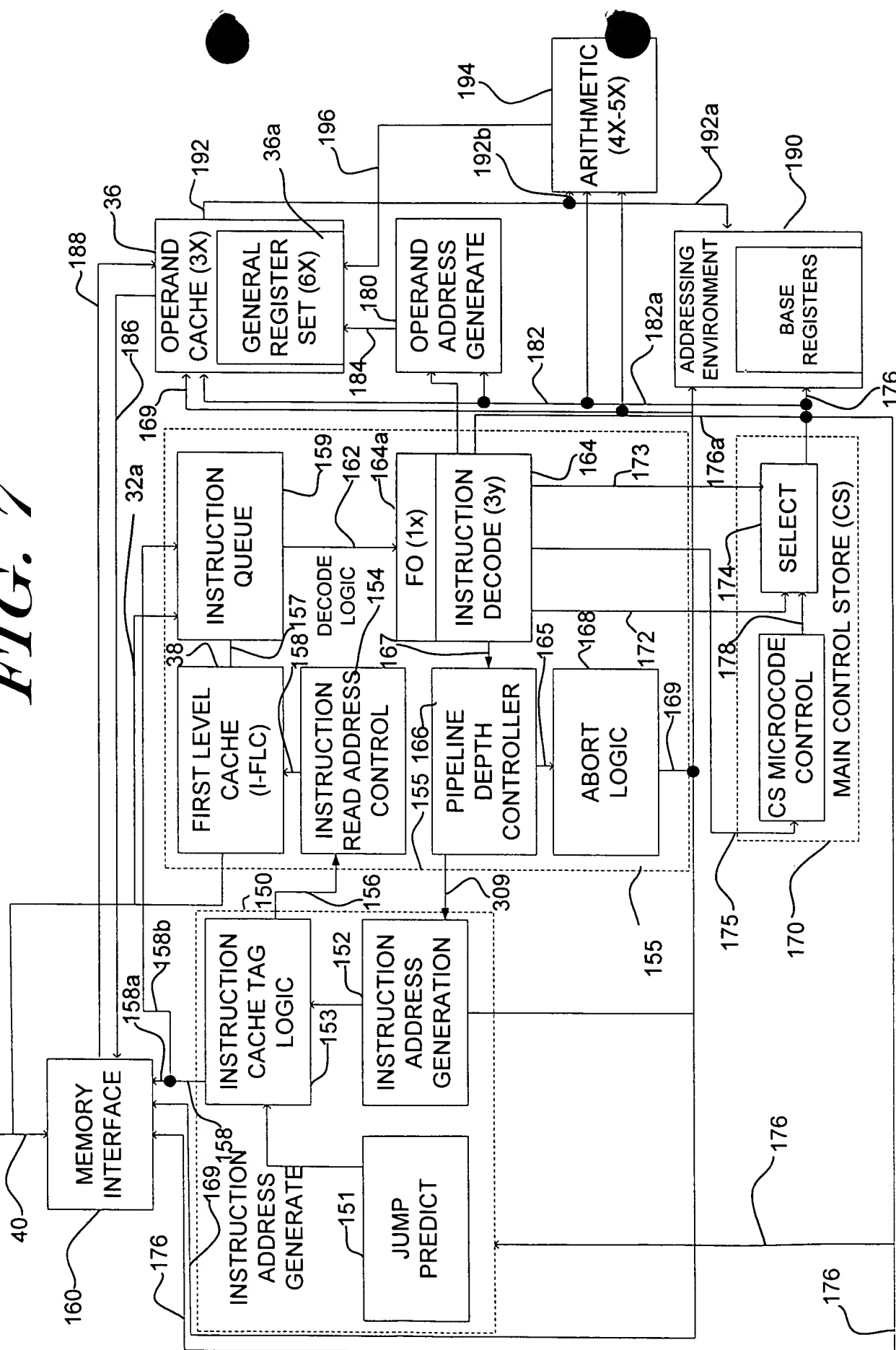


FIG. 6

**FIG. 7**



[illegible]



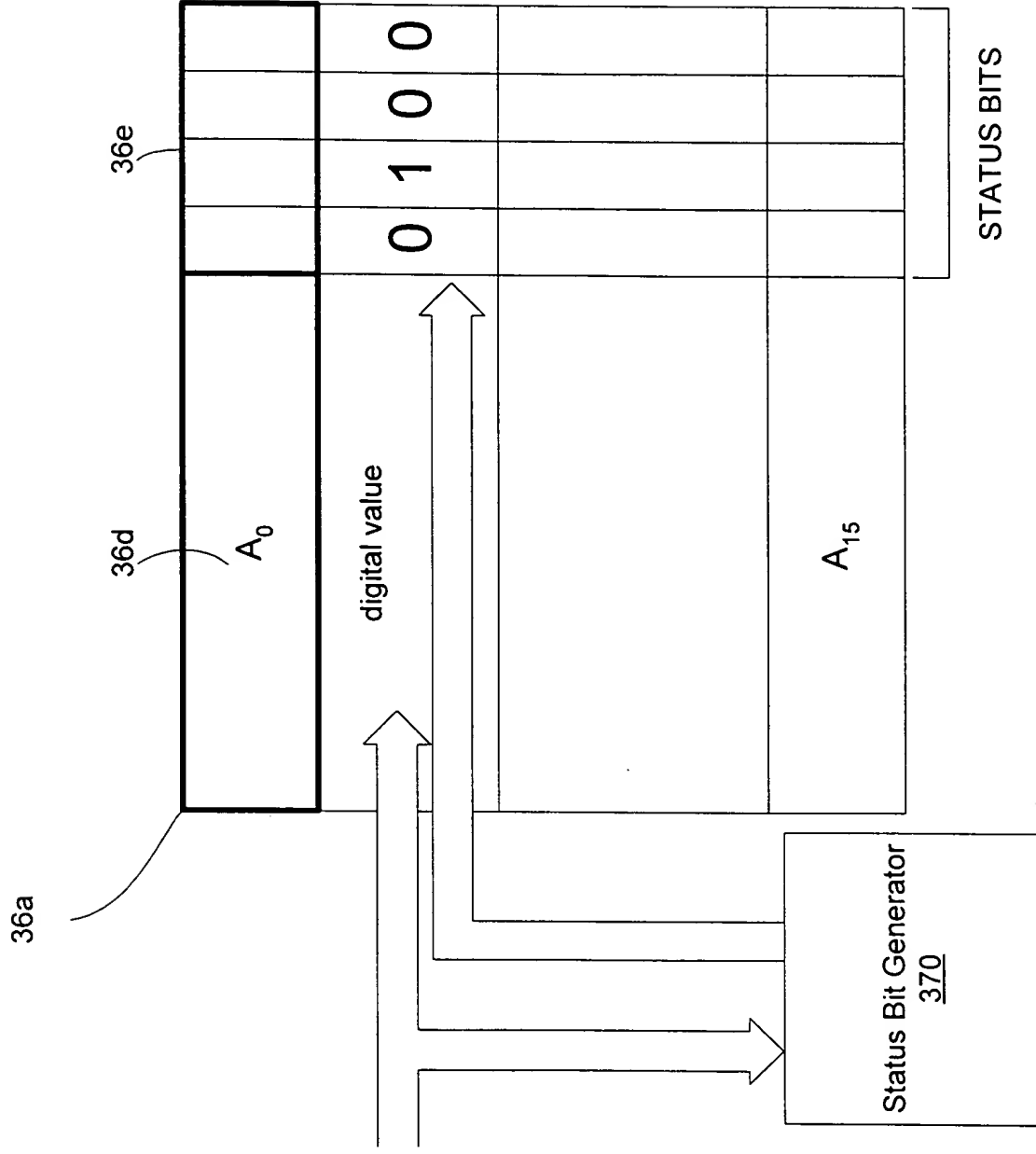


FIG. 9